The Atmel AT89S52 is an 8051 based Full Static CMOS controller with Three-Level Program Memory Lock, 32 I/O lines, 3 Timers/Counters, 8 Interrupts Sources, Watchdog Timer, 2 DPTRs, 8K Flash Memory, 256 Bytes On-chip RAM. Micromint, Inc.

LINK <https://www.datasheetarchive.com/difference%20between%208051%20and%20AT89S52-datasheet.html>